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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Wu et al.
Title: Hot Carrier Circuit Reliability Stimulation
Application No.: 09/832,933 Filing Date: April 11, 2001
Examiner: Mary C. Hogan Group Art Unit: 2123
Docket No.: BTAT.002US1 Conf. No.: 5253

Certificate of Mailing Under 37 CFR 1.8

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Signature Julien Bowe

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98, Applicants call the documents listed on the enclosed Form PTO-1449 to the Examiner's attention in this patent application.

According to 37 C.F.R. 1.98(2)(ii), copies of the U.S. Patent Documents are not required and are therefore not enclosed. Copies of the Other Art are enclosed.

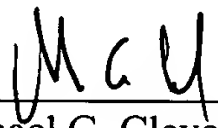
Citation of these documents shall not be construed as (1) an admission that the documents are prior art with respect to the invention or inventions claimed in this application, (2) a representation that a search has been made (other than as indicated by any cited document), or (3) an admission that the cited information is, or is considered to be, material to patentability as defined in § 1.56(b).

Attorney Docket No.: BTAT.002US1

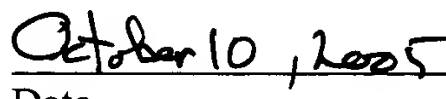
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This information disclosure statement is submitted under 37 C.F.R. § 1.97(b) and consequently no fee should be required. The Commissioner is authorized, however, to charge any fee that may be required, or to credit any overpayment, against Deposit Account No. 502664. This form is being submitted in duplicate.

Respectfully submitted,



Michael G. Cleveland
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Date

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U.S. Department of Commerce, Patent and Trademark	Atty. Docket No.	Application No.
INFORMATION DISCLOSURE STATEMENT BY APPLICANT	BTAT.002US1	09/832,933
Use several sheets if necessary)	Applicant(s)	Conf. No.
(Form PTO-1449)	Wu et al.	5253
	Filing Date	Art Group
	April 11, 2001	2123

U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	1	6,498,515	12/24/02	Kawakami et al.			
	2	6,414,498	7/2/02	Chen			

U.S. Published Patent Application Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate

Foreign Patent Documents

							Translation	
		Document	Date	Country	Class	Subclass	Yes	No

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	3	Hu, "Simulating Hot-Carrier Effects on Circuit Performance," <i>Semicond. Sci. Technol.</i> 7 (1992) B555-B558.
	4	Wu et al., "Full-Chip Reliability Simulation for VDSM Integrated Circuits," <i>Microelectronics Reliability</i> 41 (2001) pp. 1273-1278.
	5	Seo et al., "The Hot Carrier Degradation and Device Characteristics with Variation of Pre-Metal Dielectric Materials," <i>Mat. Res. Soc. Symp. Proc.</i> , Vol. 544, 1999, pp. 179-184.

Examiner

Date Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.